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		CONFIRMATION NO.
Michael Moyal	33759	8269
	EXAMINER	
PEARNE & GORDON LLP		
	ART UNIT	PAPER NUMBER
SUITE 1200 CLEVELAND, OH 44114-3108		
		EXAM

Please find below and/or attached an Office communication concerning this application or proceeding.

		T 2 32 32					
	•	Applicat	on No.	Applicant(s)			
055		09/897,3	41	MOYAL, MICHAEL			
	Office Action Summary	Examine	T	Art Unit			
<u>. </u>		Tuan T.		2816	<u> </u>		
Period fo	The MAILING DATE of this communicati or Reply	on appears on th	e cover sheet with the c	orrespondence ad	dress		
THE - External after - If the - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICAT asions of time may be available under the provisions of 37 SIX (6) MONTHS from the mailing date of this communicate period for reply specified above is less than thirty (30) day to period for reply is specified above, the maximum statutory are to reply within the set or extended period for reply will, but the property of the property o	FION. CFR 1.136(a). In no extion. ys, a reply within the stay period will apply and voy statute, cause the apply statute, cause the apply statute.	rent, however, may a reply be time tutory minimum of thirty (30) day: rill expire SIX (6) MONTHS from plication to become ABANDONE	nely filed s will be considered timel the mailing date of this or D (35 U.S.C. § 133).	y. ommunication.		
Status				•			
1)[Responsive to communication(s) filed or	n 03 October 200	93.				
	• •	This action is non-final.					
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Dispositi	on of Claims						
5)□ 6)⊠ 7)□	<u>, </u>						
Applicati	on Papers						
10)⊠	The specification is objected to by the Ex The drawing(s) filed on <u>02 July 2001</u> is/ar Applicant may not request that any objection Replacement drawing sheet(s) including the The oath or declaration is objected to by	re: a) accepte to the drawing(s) correction is requi	ne held in abeyance. See red if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CF			
Priority u	ınder 35 U.S.C. § 119						
12)⊠ a)[Acknowledgment is made of a claim for for All b) Some * c) None of: 1. Certified copies of the priority documents. 2. Certified copies of the priority documents. 3. Copies of the certified copies of the application from the International Elee the attached detailed Office action for	uments have bee uments have bee e priority docum Bureau (PCT Ru	en received. en received in Application ents have been receive e 17.2(a)).	on No ed in this National	Stage		
Attachmen	` '						
	e of References Cited (PTO-892)		4) Interview Summary				
3) 🔯 inforr	e of Draftsperson's Patent Drawing Review (PTO-9 nation Disclosure Statement(s) (PTO-1449 or PTO/ r No(s)/Mail Date <u>8/27/2001</u> .		Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:)-152)		

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DETAILED ACTION

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Specification

1. Applicant is reminded of the proper language and format for an abstract of the disclosure.

The abstract should be in narrative form and generally limited to a single paragraph on a separate sheet within the range of 50 to 150 words. It is important that the abstract not exceed 150 words in length since the space provided for the abstract on the computer tape used by the printer is limited. The form and legal phraseology often used in patent claims, such as "means" and "said," should be avoided. The abstract should describe the disclosure sufficiently to assist readers in deciding whether there is a need for consulting the full patent text for details.

The language should be clear and concise and should not repeat information given in the title. It should avoid using phrases which can be implied, such as, "The disclosure concerns," "The disclosure defined by this invention," "The disclosure describes," etc.

Drawings

2. Figures 1-2 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 112

- 3. The following is a quotation of the second paragraph of 35 U.S.C. 112:
 - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 4. Claims 7-8 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 7, the recitation of "two FETs forming a voltage divider" is indefinite because it is unclear as to what are these two FETs being referred to. Clarification is required.

Claim 8 is indefinite because of the technical deficiencies of claim 7.

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Claim Rejections - 35 USC § 102

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 6. Claims 1-11 are rejected under 35 U.S.C. 102(b) as being anticipated by Chen et al. (USP 5,363,066). Figure 2D shows a controllable current source comprising a first and a second supply terminal (VDD, Vss), an output terminal (Vo), first current path (208, 216), second current path (220, 210), each current path having a supply side current control device (214, 216; 218, 220), activated by a current control signal (U, UB; D, DB), as well as an output side current adjustment device (208, 210) in series to the current control device, characterized in that the current paths are each allocated a potential adjustment device (184, 188, 192; 198, 202, 196), by means of which, when the current control device of the current path is inactive (216, 220) is inactive, a predetermined adjustment potential (potentials at a node between 188, 184; and between 202 and 196), whose value lies between VDD and Vss, is applied to an intermediate node (junction of 208, 216; 210, 220) lying between the current control device and the current adjustment device as called for in claim 1.

Regarding claim 2, the current control device (214, 216; 218, 220) has at least one FET whose forms a section of the current path.

Regarding claim 3, the current adjustment device (208, 210) has at least one FET, whose channel forms a section of the current path.

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Regarding claim 4, junction voltage of 188 and 184 is different from the junction voltage of 202 and 196.

Regarding claim 5, junction voltage of 202 and 196 is closer to VDD than the junction voltage of 184 and 188.

Regarding claim 6, the adjustment potential for one of the two current paths is approximately equal to that potential which is present at the intermediate section of this current path when the current control device of this current path is activated.

Regarding claims 7 and 8 transistors 214, 208; transistors 218, 210) forms voltage divider.

Regarding claim 9, down stream integrator is the loop filter 17.

Regarding claim 10, down stream VCO (not shown, see column 1, lines 25-30) is connected to the loop filter 17.

Regarding claim 11, the phase locked loop (column 1, lines 15-30) comprises a phase detector (16 of figure 2A), VCO (column 1, lines 15-30), feedback path (phase locked loop inherently having feedback for feeding the output of the VCO to the phase detector).

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. In this regard, applicant's cited prior art has been carefully considered.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tuan T. Lam whose telephone number is 571-272-1744. The examiner can normally be reached on Monday to Friday (7:30 am to 6:00pm).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, TIMOTHY P CALLAHAN can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Tuan T. Lam

Primary Examiner
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2/26/2004